

Towards the Synthesis of Provably Secure Programs

(work-in-progress)

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What is **correctness**?

Let \mathcal{P} , \mathcal{Q} be logical formulas, c be an imperative program, the Hoare triple $\{\mathcal{P}\}c\{\mathcal{Q}\}$ holds.

Problem of deductive **verification**:

Given \mathcal{P} , \mathcal{Q} , and c , show that $\vdash \{\mathcal{P}\}c\{\mathcal{Q}\}$.

Problem of deductive **synthesis**:

Given \mathcal{P} and \mathcal{Q} , find a program c such that $\models \{\mathcal{P}\}c\{\mathcal{Q}\}$.
(and its coherent proof $\vdash \{\mathcal{P}\}c\{\mathcal{Q}\}$)

Security obligation:

- The program must also satisfy a security invariant \mathcal{I}_S .
(e.g., information confidentiality, memory isolation, ...)
- Find a program c such that $\models \{\mathcal{P} \wedge \mathcal{I}_S\} c \{Q \wedge \mathcal{I}_S\}$.

$$\text{Synthesizer : } \underbrace{\langle \langle \mathcal{P}, \mathcal{Q} \rangle, \mathcal{I}_S \rangle}_{\substack{\text{functionality} \\ \text{spec}} \times \substack{\text{security} \\ \text{spec}}} \mapsto \underbrace{\langle c, p \rangle}_{\text{program} \times \text{proof}}$$

Challenge: “Synthesis in the Large” is hard.

COMPOSITIONALITY. If two programs preserve the same security invariant \mathcal{I}_s , then the sequential composition of them preserves \mathcal{I}_s .

i.e.,

$$\frac{\{P \wedge \mathcal{I}_s\} c_1 \{Q \wedge \mathcal{I}_s\} \quad \{Q \wedge \mathcal{I}_s\} c_2 \{R \wedge \mathcal{I}_s\}}{\{P \wedge \mathcal{I}_s\} c_1; c_2 \{R \wedge \mathcal{I}_s\}}.$$

Idea:

- Decompose specs and programs:

$$P_0, P_1, \dots, P_n, Q \quad \text{and} \quad C = c_0; c_1; \dots; c_n$$

- “Synthesis in the Small” is feasible!

Deductive program synthesis

Prior work (state-of-the-art):

- Proof-theoretic synthesis by invariant inference (Srivastava et al. POPL'10)
- *Synthetic* Separation Logic (Polikarpova and Sergey POPL'19)

What's in the future?

A program synthesis framework with *security* obligations

- for a simple heap-manipulating imperative language
- program and proof co-generation

Potential synthesizing targets:

- trustworthy embedded system components
- simple separation kernel targeting RISC-V
- (many more. . .)

Thanks.