F28HS Hardware-Software Interface: Systems Programming

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⁰No proprietary software has been used in producing these slides = 🕞 🤟



Outline

- Tutorial 1: Using Python and the Linux FS for GPIO Control
- 2 Tutorial 2: Programming an LED Display
- 3 Tutorial 3: Programming a Button input device
 - Tutorial 4: Inline Assembler with gcc
 - Tutorial 5: Using the on-chip timer
 - C library timers
 - Timers with assembler-level system calls
 - Timers by probing the RPi on-chip timer
 - Tutorial 6: Programming an LCD Display
 - Tutorial 7: Code Security: Buffer Overflow Attacks
 - Tutorial 8: Performance Counters on the RPi 2

Tutorial 3: Programming a Button input device

- In this tutorial we want to use a button, connected through a breadboard as an input device.
- This is the simplest input device that we will cover.
- The code needed to control is typical for such devices.
- This tutorial deals with programming a button as input device.



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- In the LED tutorial, we have seen that we first need to identify the registers that give control to the device.
- For that we will again look into the BCM Peripherals documentation.
- We will then go through a simple example of
 - reading button input data,
 - blinking an LED on button press.
- We want to connect the button with pin 24, using a breadboard.
- These simple activities, will also be at the core of CW2.



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GPIO Register Assignment

GPLEV0	GPIO Pin Level 0	32	R
GPLEV1	GPIO Pin Level 1	32	R
-	Reserved	-	-
GPEDS0	GPIO Pin Event Detect Status 0	32	R/W
GPEDS1	GPIO Pin Event Detect Status 1	32	R/W
-	Reserved	-	-
GPREN0	GPIO Pin Rising Edge Detect Enable 0	32	R/W
GPREN1	GPIO Pin Rising Edge Detect Enable 1	32	R/W
-	Reserved	-	-
GPFEN0	GPIO Pin Falling Edge Detect Enable 0	32	R/W
GPFEN1	GPIO Pin Falling Edge Detect Enable 1	32	R/W
	GPLEV0 GPLEV1 - GPEDS0 GPEDS1 - GPREN0 GPREN1 - GPFEN0 GPFEN1	GPLEV0 GPIO Pin Level 0 GPLEV1 GPIO Pin Level 1 - Reserved GPEDS0 GPIO Pin Event Detect Status 0 GPEDS1 GPIO Pin Event Detect Status 1 - Reserved GPREN0 GPIO Pin Rising Edge Detect Enable 0 GPREN1 GPIO Pin Rising Edge Detect Enable 1 - Reserved GPFEN0 GPIO Pin Falling Edge Detect Enable 1 GPFEN1 GPIO Pin Falling Edge Detect Enable 1	GPLEV0 GPIO Pin Level 0 32 GPLEV1 GPIO Pin Level 1 32 - Reserved - GPEDS0 GPIO Pin Event Detect Status 0 32 GPEDS1 GPIO Pin Event Detect Status 1 32 - Reserved - GPREN0 GPIO Pin Rising Edge Detect Enable 0 32 GPREN1 GPIO Pin Rising Edge Detect Enable 1 32 - Reserved - GPFEN0 GPIO Pin Falling Edge Detect Enable 1 32 GPFEN0 GPIO Pin Falling Edge Detect Enable 0 32 GPFEN1 GPIO Pin Falling Edge Detect Enable 1 32

⁰See BCM Peripherals Manual, Chapter 6, Table 6.1

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BCM2835 GPIO Peripherals

Base adress: 0x3F000000

0	GPESEI	Pins 0-9	(3-bits per pin)
5	SHIEL	Pins 50-53	(
7 8	GPSET	Pins 0-31 Pins 32-53	(1-bit per pin)
10 11	GPCLR	Pins 0-31 Pins 32-53	(1-bit per pin)
13 14	GPLEV	Pins 0-31 Pins 32-53	(1-bit per pin)

The main registers that we need in this case are (see p90ff of BCM2835 ARM peripherals):

- GPFSEL: function select registers (3 bits per pin); set it to 0 for input, 1 for output; 6 more alternate functions available
- GPSET: set the corresponding pin
- GPCLR: clear the corresponding pin
- GPLEV: return the value of the corresponding pin

Define the button pin as an INPUT device

	Write into these bits (12–14) to set the function for pin 24																								
	0:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8
	1:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8
	2:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	- p	in 2	2 <mark>4</mark> 2	11	10	9	8
	3:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8
	4:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8
	5:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8
	6:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8
	7:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8
	8:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	ε
	9:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8
•	10:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8
•	11:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8
•	12:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9 ERIO	3 т
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	1:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8
	2	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8
	3:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8
	4:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8
	5:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8
	6:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8
	7:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8
	8:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8
	9:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8
	10:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8
	11:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8
	12:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8
	IB:	R	eca	all. (eac	h (GPI	FSI	EL	reo	iiste	er o	con	trol	s 1	0 r	oins	s (1	-1	0. 1	1-	20.	et	ERIO WAT	T
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Define the button pin as an INPUT device



Contents:

GPIO registers (Base address: 0x3F200000)

Bit positions

-		• •	- 3.			1-		-							•••	- /							
4:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9
5:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9
6:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9
7:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9
8:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9
9:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9
10:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9
11:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9
12:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9
iPLE	V0	30	29	28	27	26	2 p	in 2	24 3	22	21	20	19	18	17	16	15	14	13	12	11	10	9
14:	31	30	29	28	27	26	2 5	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9
15:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9

Read this bit (24)



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Contents:

GPIO registers (Base address: 0x3F200000)

Bit positions



Read from this bit (24)







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Contents: Bit values GPIO registers (Base address: 0x3F200000) 4 5 6 7 8 9 10 11 12 13 0 14 15 Input: LOW HERIOT WATT

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First we define some constants that we will need.

```
// Tunables:
// PINs (based on BCM numbering)
#define LED 23
#define BUTTON 24
// delay for loop iterations (mainly), in ms
#define DELAY 200
#define INPUT
                                   \cap
#define OUTPUT
                                   1
#define LOW
                                   0
#define HIGH
                                   1
```

This assumes that we have wired-up the button with GPIO pin 24 and the LED with GPIO pin 23.

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Using a breadboard

To control an external LED, you could directly connect GPIO pins with the LED and a resistor using jumper cables.

However, a breadboard is a more flexible way of wiring peripherals, such as LEDs or buttons, to the RPi.

You need to understand how the columns and the rows on a breadboard are connected, though.



For a good basic intro on how to use a breadboard follow this link



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The wiring as a Fritzing diagram

To describe a specific wiring, we use Fritzing diagrams like this:



An LED, as output device, is connected to the RPi2 using GPIO pin 23.

A button, as input device, is connected to GPIO pin 24.

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We memory-map the addresses for the GPIO registers (as before).

```
gpiobase = 0x3F200000;
// memory mapping
if ((fd = open ("/dev/mem", O_RDWR | O_SYNC |
  O CLOEXEC) ) < 0)
 return failure (FALSE, "setup: Unable to open /
     dev/mem:_%s\n", strerror (errno)) ;
// GPTO:
qpio = (uint32 t *)mmap(0, BLOCK SIZE, PROT READ)
  PROT WRITE, MAP SHARED, fd, qpiobase);
if ((int 32 t)qpio == -1)
  return failure (FALSE, "setup: mmap. (GPIO).
     failed: %s\n", strerror (errno));
```

We set the modes for the LED pin (OUTPUT) and the button pin (INPUT).

// setting the mode fSel = 2; // register 2 (GPFSEL2) shift = 9; // slot 3 (shift 3*3) // set the above pin to output mode *(qpio + fSel) = (*(qpio + fSel) & ~(7 << shift)) | (1 << shift) ; // Sets bits to one = output fSel = 2; // register 2 (GPFSEL2) shift = 12; // slot 4 (shift 4*3) // set the above pin to input mode *(qpio + fSel) = (*(qpio + fSel) & ~(7 << shift)) ; // Sets bits to zero = input

Tutorial 3: Prging a Button

Inside the main loop, we first read from the bit associated with the button input in the GPLEV0 register.



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Further down the loop, we write to the bit associated with the LED output in the GPLCR0 or GPSET0 register.

```
if (theValue == LOW) {
 clrOff = 10; // GPCLR0 for pin 23
  *(qpio + clrOff) = 1 << (LED & 31); // 23-rd bit
      in the register
} else {
  setOff = 7; // GPSET0 for pin 23
  *(qpio + setOff) = 1 << (LED & 31); // 23-rd bit
      in the register
}
// delav ...
```

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Finally, we want to clean-up by setting the LED to LOW. Which kind of code do we need here?

// clean-up by setting the LED pin to LOW



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Summary

- Reading input from a button works in the same way as writing to the LED:
 - We need to identify the relevant registers and bits for our pin
 - ► We declare the pin an INPUT device in the GPFSEL register
 - We read from the associated bit in the GPLEV register to get the input
- With the button you have a basic input device to communicate with the system
- In the CW we will combine a button (for input), an LED (for output) and an LCD display (for nicer output) and implement a small app for this configuration.

See sample source: tut_button.c